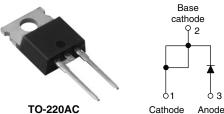


Vishay High Power Products

High Performance Schottky Generation 5.0, 15 A



TO-220AC	Cathode	Anode

15 A

100 V

0.67 V

PRODUCT SUMMARY

I_{F(AV)}

 V_{R}

V_F at 15 A at 125 °C

FEATURES

- 175 °C high performance Schottky diode
- Very low forward voltage drop
- · Extremely low reverse leakage
- Optimized V_F vs. I_R trade off for high efficiency
- · Increased ruggedness for reverse avalanche capability
- RBSOA available
- Negligible switching losses
- Submicron trench technology
- Full lead (Pb)-free and RoHS compliant devices
- Designed and qualified for industrial level

APPLICATIONS

- High efficiency SMPS
- · Automotive
- · High frequency switching
- Output rectification
- · Reverse battery protection
- · Freewheeling
- · Dc-to-dc systems
- · Increased power density systems

MAJOR RATINGS AND CHARACTERISTICS						
SYMBOL	CHARACTERISTICS	VALUES	UNITS			
V _{RRM}		100	V			
V _F	15 Apk, T _J = 125 °C (typical)	0.63	V			
T _J	Range	- 55 to 175	°C			

VOLTAGE RATINGS						
PARAMETER	SYMBOL	TEST CONDITIONS	15TT100	UNITS		
Maximum DC reverse voltage	V _R	T _J = 25 °C	100	V		

ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	TEST COND	VALUES	UNITS		
Maximum average forward current	I _{F(AV)}	50 % duty cycle at T _C = 144 °C, rectangular waveform		15		
Maximum peak one cycle non-repetitive surge current	10	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated	920	A	
	IFSM	10 ms sine or 6 ms rect. pulse	V _{RRM} applied	240		
Non-repetitive avalanche energy	E _{AS}	T _J = 25 °C, I _{AS} = 1.1 A, L = 60 mH		36	mJ	
Repetitive avalanche current	I _{AR}	Limited by frequency of operation and time pulse duration so that $T_J < T_J$ max. I_{AS} at T_J max. as a function of time pulse See fig. 8		I _{AS} at T _J max.	А	

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15TT100

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ELECTRICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CONDITIONS			MAX.	UNITS	
		15 A	T 05 °C	-	0.81	V	
Earward voltage drop	V _{FM} ⁽¹⁾	30 A	T _J = 25 °C	-	0.92		
Forward voltage drop	VFM (')	15 A	T _ 105 °C	-	0.67		
		30 A	- T _J = 125 °C	-	0.79		
Deverage legitage gurrent	I _{RM} ⁽¹⁾	T _J = 25 °C	V Datad V	=	120	μΑ	
Reverse leakage current		T _J = 125 °C	V _R = Rated V _R	=	5	mA	
Junction capacitance	C _T	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		550	-	pF	
Series inductance	L _S	Measured lead to lead 5 mm from package body		8.0	-	nΗ	
Maximum voltage rate of change	dV/dt	Rated V _R		-	10 000	V/µs	

Note

 $^{^{(1)}\,}$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and storage temperature range	је	T _J , T _{Stg}		- 55 to 175	°C	
Maximum thermal resistance, junction to case		R _{thJC}	DC operation	2.5	0000	
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.5	°C/W	
Approximate weight				2	g	
Approximate weight				0.07	OZ.	
Mounting torque	minimum			6 (5)	kgf ⋅ cm	
Mounting torque	maximum			12 (10)	(lbf \cdot in)	
Marking device			Case style TO-220AC	15TT100		

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High Performance Vishay High Power Products Schottky Generation 5.0, 15 A

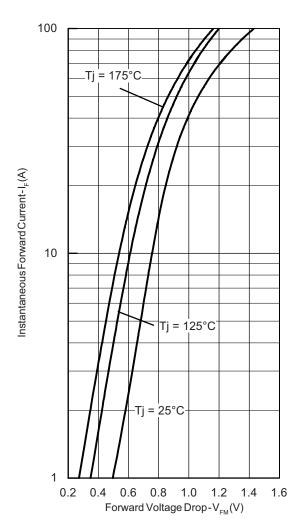


Fig. 1 - Maximum Forward Voltage Drop Characteristics

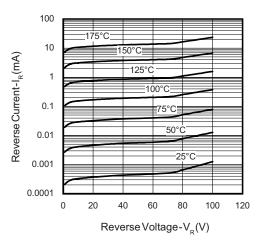


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

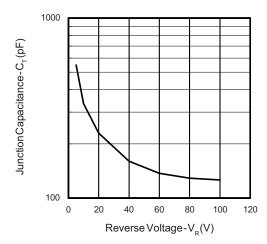


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

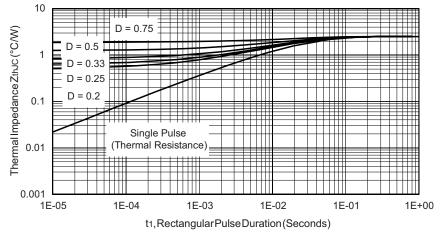


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

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High Performance Schottky Generation 5.0, 15 A



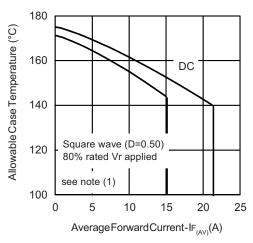


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

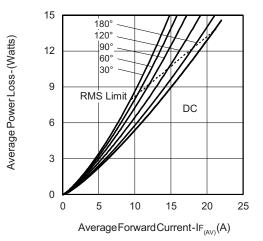


Fig. 6 - Forward Power Loss Characteristics

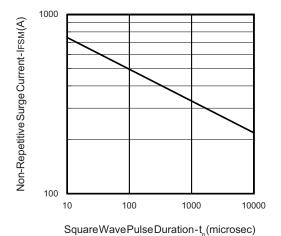


Fig. 7 - Maximum Non-Repetitive Surge Current

Note



High Performance Vishay High Power Products Schottky Generation 5.0, 15 A

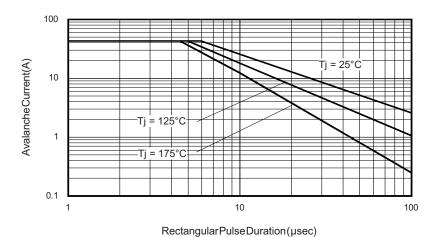


Fig. 8 - Reverse Bias Safe Operating Area (Avalanche Current vs. Rectangular Pulse Duration)

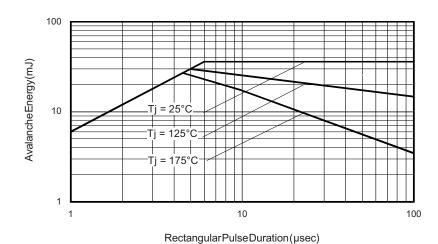


Fig. 9 - Reverse Bias Safe Operating Area (Avalanche Energy vs. Rectangular Pulse Duration)

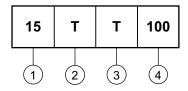
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High Performance Schottky Generation 5.0, 15 A



ORDERING INFORMATION TABLE





1 - Current rating (15 A)

2 - Package:

T = TO-220

3 - T = Trench

4 - Voltage code (100 V)

Tube standard pack quantity: 50 pieces

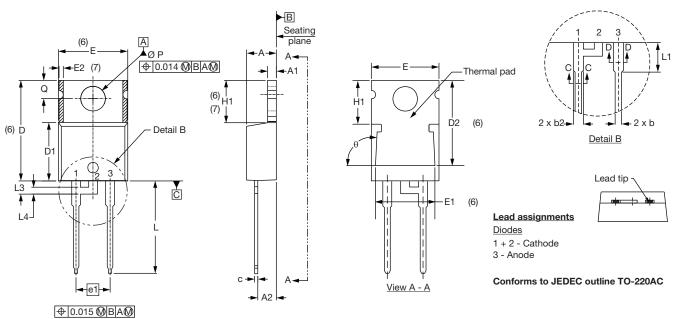
LINKS TO RELATED DOCUMENTS				
Dimensions http://www.vishay.com/doc?95221				
Part marking information	http://www.vishay.com/doc?95224			



Vishay Semiconductors

TO-220AC

DIMENSIONS in millimeters and inches



SYMBOL	MILLIM	MILLIMETERS		INCHES	
STWIDOL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.25	4.65	0.167	0.183	
A1	1.14	1.40	0.045	0.055	
A2	2.56	2.92	0.101	0.115	
b	0.69	1.01	0.027	0.040	
b1	0.38	0.97	0.015	0.038	4
b2	1.20	1.73	0.047	0.068	
b3	1.14	1.73	0.045	0.068	4
С	0.36	0.61	0.014	0.024	
c1	0.36	0.56	0.014	0.022	4
D	14.85	15.25	0.585	0.600	3
D1	8.38	9.02	0.330	0.355	
D2	11.68	12.88	0.460	0.507	6
Е	10.11	10.51	0.398	0.414	3, 6

SYMBOL	MILLIM	IETERS	INC	HES	NOTES
STINIBUL	MIN.	MAX.	MIN.	MAX.	NOTES
E1	6.86	8.89	0.270	0.350	6
E2	-	0.76	-	0.030	7
е	2.41	2.67	0.095	0.105	
e1	4.88	5.28	0.192	0.208	
H1	6.09	6.48	0.240	0.255	6, 7
L	13.52	14.02	0.532	0.552	
L1	3.32	3.82	0.131	0.150	2
L3	1.78	2.13	0.070	0.084	
L4	0.76	1.27	0.030	0.050	2
ØΡ	3.54	3.73	0.139	0.147	
Q	2.60	3.00	0.102	0.118	
θ	90° t	o 93°	90° t	o 93°	

Notes

- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Lead dimension and finish uncontrolled in L1
- (3) Dimension D, D1 and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Dimension b1, b3 and c1 apply to base metal only
- (5) Controlling dimension: inches
- (6) Thermal pad contour optional within dimensions E, H1, D2 and E1
- (7) Dimension E2 x H1 define a zone where stamping and singulation irregularities are allowed
- (8) Outline conforms to JEDEC TO-220, D2 (minimum) where dimensions are derived from the actual package outline





Vishay

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